

ing and inference of neural networks has led to the search for algorithms and hardware platforms that enable fast, low-latency, and power-efficient processing. Photonic implementations of neural network processors, specifically those implemented in a silicon-on-insulator (SOI) platform, commonly referred to as Photonic Neural Networks (PNNs), have emerged as a compelling solution to address these critical needs. However, PNNs currently face scalability issues related to the size of optical devices, the number of electrical inputs required to control and reconfigure the on-chip optical devices, and the power required for control [2]. Implementing weights in photonic neural networks by employing microring resonators (MRRs) is one of the most commonly employed approaches. This approach uses a ring implemented in a broadcast-and-weight architecture to implement one weight resulting in inefficient usage of

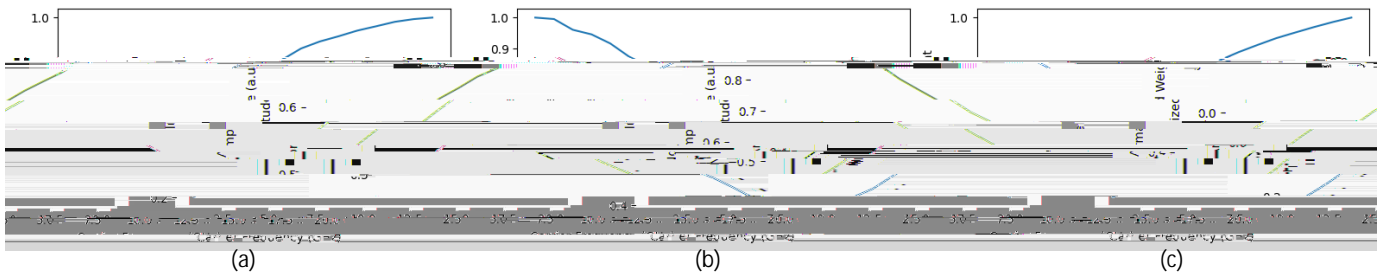


Fig. 2. Signal amplitude versus RF carrier frequency for (a) Thru port (b) Drop port. (c) Balanced output (Thru - Drop)

Double-sideband suppressed-carrier (DSB-SC) amplitude modulation is used for the RF carrier modulation. The optical carrier frequency is chosen to be exactly centered at the resonance of the MRR to allow symmetric weighting of the optical sidebands. At the Drop and Thru RF outputs, the RF signals are converted to baseband using a square-law RF detector implemented in software but which may be implemented in hardware using a mixer followed by a low-pass filter. To obtain the actual weights, the output of the Drop port is subtracted from that of the Thru port.

### III. RESULTS

Figure 2 shows the weight of a signal as a function of its carrier frequency. As expected, for lower RF carrier frequencies, most of the light is being routed to the Drop port hence imparting a negative weight on the signal. On the other hand, for higher RF carrier frequencies, most of the light is being routed to the Thru port which corresponds to a positive weight.

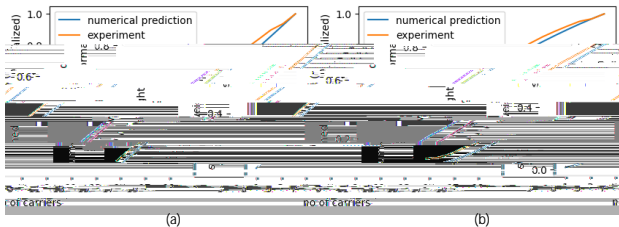


Fig. 3. Normalized summed output versus number of carrier frequencies for (a) Thru port (b) Drop port.

Figure 3 demonstrates the ability for several weights to be implemented using one microring. Increasing number of integer spaced carrier frequencies are combined and the output amplitude is measured. The carrier frequencies are chosen such that 5 carrier corresponds to carrier frequencies 1,2,3,4, and 5 GHz and 20 carriers corresponds to 1,2,3,...,20 GHz. The amplitude at the Thru and Drop ports are compared with the expected sum of the amplitudes of each weighted carrier as obtained from the single weight experiment. We observe good agreement between the predicted and actual output. Deviations may be attributed to slight changes in the position of the optical carrier relative to MRR's resonance and phase deviations of each sideband at the output. Phase deviations

may be corrected by calibrating the phase at the input so that the phases at the output are aligned.

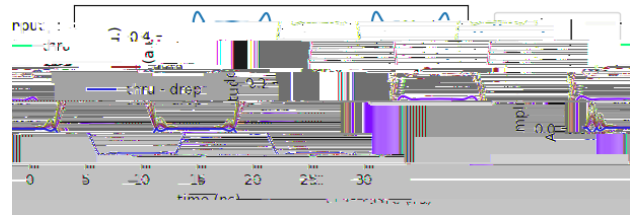


Fig. 4. Simulation result for a 1 GHz signal modulated on a 5 GHz carrier.

Finally, we simulate the weighting process in software. Figure 4 shows the result of a series of 1 GHz pulses modulated on a 5 GHz carrier. As seen in the figure, the original signal is multiplied by a negative weight as expected.

### IV. CONCLUSION

In this work, we present a technique for carrying out multiple multiply-accumulate operations simultaneously using a single MRR. We demonstrate twenty (20) simultaneous weights using one MRR. Experimental results agree with the expected computational predictions indicating the feasibility of this technique. This technique can be used to improve the compute density of the linear frontend of photonic neural networks.

### REFERENCES

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