Silicon Photonic Modulator Neuron

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There has been recent interest in neuromorphic photonics, a field with the promise to access pivotal and unexplored regimes of machine intelligence. Progress has been made on isolated neurons and analog interconnects; nevertheless, this renewal of interest has yet to produce a demonstration of a silicon photonic neuron capable of interacting with other like neurons. We report a modulator-class photonic neuron fabricated in a conventional silicon photonic process line. We demonstrate the behaviors of transfer-function configurability, fan-in, inhibition, time-resolved pulse processing, and, crucially, autaptic cascadability—a su cient set of behaviors for a device to act as a neuron participating in a network of like neurons. The silicon photonic modulator neuron constitutes the final piece needed to make photonic neural networks fully integrated on currently available silicon photonic platforms.

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I. INTRODUCTION

Renewed interest in neuromorphic photonics has been heralded by advances in photonic integration technology [\[1–](#page-14-0)[3\]](#page-14-1), roadblocks in conventional computing performance [\[4,](#page-14-2)[5\]](#page-14-3), the return of neuromorphic electronics $[6-10]$ $[6-10]$, and the inundation of machine learning (ML) with neural models [\[11\]](#page-14-6). Neural networks have played some role in ML (e.g., image and voice recognition, language translation, pattern detection, and others) since the 1950s [\[12,](#page-14-7)[13\]](#page-14-8). They fell out of favor in the 1990s because they are di cult to train.

Over the past decade, neural network models have decisively retaken the helm of ML under the alias of "deep networks" [\[14\]](#page-14-9). There are three main reasons: (1) major algorithmic innovations [\[15,](#page-14-10)[16\]](#page-14-11), (2) the Internet—an inexhaustible source of millions of training examples—and (3) new hardware, specifically graphical processing units (GPUs) [\[17\]](#page-14-12). Central processing units (CPUs) are woefully ine cient at evaluating these models because they are centralized and instruction based, whereas networks are distributed and capable of adaptation without a programmet331PU9 Ute9th16(6)tatte4046blne) today, even they have064043-1 © 2019 American Physical Society been pushed to their limits [\[18\]](#page-14-13).

Today's demand for evaluating neural network models necessitates new hardware. High-tech behemoths and research agencies—notably IBM [\[6\]](#page-14-4), HP [\[19\]](#page-14-14), Intel [\[10\]](#page-14-5), Google $[20,21]$ $[20,21]$, the Human Brain Project $[22]$, and DARPA SyNAPSE [\[23\]](#page-14-18)—have invested heavily in massively parallel application-specific integrated circuits $(ASICs)$ for evaluating neural network models more e $$ ciently. Some of these architectures aim to be ML number crunchers [\[20,](#page-14-15)[24\]](#page-14-19) and others have enabled novel neuroscientific tools [\[25,](#page-14-20)[26\]](#page-14-21) and previously unforeseen low-power mobile applications [\[27\]](#page-15-0).

The primary performance driver for the neuromor-

Moving beyond the nanosecond will require moving beyond purely electronic physics.

Photonic physics exhibit properties distinct from those of electronics in terms of multiplexing, energy dissipation, and cro[ss ta](#page-14-20)lk. These properties are favorable for dense, high-bandwidth i[nter](#page-15-1)connects [29] in addition to configurable analog signal processing [30–32]. Consequently, neuromorphic photonic systems could operate 6–8 orders of magnitude faster than neuromorphic electronics [33]

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with potentially higher energy e ciencies [\[34\]](#page-15-2). Neural interconnects based on field evolution in free space [\[35,](#page-15-3) [36\]](#page-15-4), holograms [\[37,](#page-15-5)[38\]](#page-15-6), and fiber [\[39\]](#page-15-7) have been demonstrated but have not been widely adopted, in part because they cannot be integrated and thereby scaled robustly and manufactured cheaply. Analog interconnects integrate[d](#page-15-8) [on](#page-15-8)

II. METHODS

A. Device description

1. The f *i* e q

The modulator neuron is an optical-to-electrical-tooptical (O-E-O) device consisting of two photodetectors

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There are three signal generators used in the following experiments, two analog (a.k.a. synths) and one binary. A simple slow-wave-form generator (HP 8116A) is used to acquire the transfer functions (Sec. [III A\)](#page-5-0) and the autapse behavior (Sec. [III E\)](#page-8-0). The $8116A$ o ers control of sawtooth wave forms that can be used to separate rising and falling aspects. Burst inputs are generated by a Rohde and Schwartz SMBV 100A VG (R&S), which is used in Secs. [III A,](#page-5-0) [B,](#page-6-0) and [C.](#page-11-0) The R&S burst can also be viewed as trains of return-to-zero (RZ) pulses of varying amplitude. The binary-pulsed inputs used in Sec. [III D](#page-11-1) are generated by a pulse-pattern generator (PPG) (Anritsu MP1761B). The PPG provides the highest instantaneous bandwidth but the least control over wave forms.

The neuron's output is coupled o -chip, detected, and observed in a sampling oscilloscope (Tektronix DSA8300). Between the output coupler and the oscilloscope, there is a signal-to-noise enhancement stage, not diagrammed, consisting of an erbium-doped fiber amplifier (EDFA), an optical band-pass filter at λ_n , a is widely used in feedforward-machine-learning networks today, i.e., in multilayer perceptrons (MLPs) and convolutional neural networks (CNNs) [\[20\]](#page-14-15). Positive and negative ReLUs are obtained by biasing slightly o resonance, either above or below the pump wavelength. A network that combines sigmoid and ReLU neurons is well suited to solving nonlinear optimization problems with constraints, some of which are reviewed in Ref. [\[69\]](#page-16-0). The peaked transfer functions of Figs. $5(e)$ and $5(f)$ are known as radial basis functions (RBFs). When biased on resonance, the RBF is centered at zero, resulting in a quadratic or rectifying transfer function. The o -centered RBF is obtained by setting the electrical bias to achieve the highest resonator *Q* is central to the idea of network-based processing, so it is particularly important to demonstrate this feature directly

feedback signal to influence the same neuron. In other words, the experiment also verifies the presence of a fan-in mechanism.

There are several works that have successfully approached cascadability by avoiding all-optoelectronic signal pathways and instead using an O-E-O chain consisting of a photodetector connected to a laser [\[48,](#page-15-9)[81](#page-16-1)[,82\]](#page-16-2) or modulator [\[53,](#page-15-10)[71\]](#page-16-3). Wavelength constraints and phase sensitivity vanish because this information is lost in the electronic domain. In addition, the E-O conversion step can o er strong nonlinearity, as employed here, and the electronic domain itself o ers e cient mechanisms for nonlinearity and amplification. In Ref. [\[83\]](#page-16-4), an O-E-O neuron based on cryogenic silicon LEDs, superconducting detectors, and superconducting amplifiers [\[84\]](#page-16-5) was proposed. Its physical cascadability was demonstrated by the E-O-E LED-detector link shown in Ref. [\[85\]](#page-16-6), and its gain cascadability has been addressed in more recent simulation works [\[81,](#page-16-1)[82\]](#page-16-2). A potential downside of O-E-O is a vulnerability to electrical parasitics; however, these parasitics can remain small regardless of the network scale because O-E-O occurs entirely within a neuron, not between neurons.

When light combines, it interferes, posing a fundamental challenge to fan-in [\[75\]](#page-16-7). Optical fan-in results in either phase dependence, when coherent, or *N*-fold loss, when incoherent (e.g., 3 dB at $N = 2$). In some all-optical devices where the in-out wavelengths can be the same (cascadable), these wavelengths also must be the same, meaning they cannot have more than one input [\[44,](#page-15-11)[79\]](#page-16-8). Fan-in with coherent signals can be achieved by exerting complete control of the optical phase in the interconnect [\[41\]](#page-15-12) but then signal-dependent phase changes in a neuron profoundly a ect the behavior of the subsequent interconnect, precluding any cascadability. In Ref. [\[41\]](#page-15-12), neuron calculations were implemented at low speed in a CPU; a neuron based on saturable absorption was contemplated, but it was not discussed how this element would regenerate a consistent optical phase.

Fan-in has also been achieved using inputs that are coherent but mutually incoherent, such as di erent spatial modes $[86,87]$ $[86,87]$, dierent polarizations, or dierent wavelengths [\[66](#page-16-11)[,88,](#page-16-12)[89\]](#page-17-0). These signals do not interfere and, since they are individually coherent, can be multiplexed and routed and/or weighted independently by tunable resonators [\[58](#page-16-13)[,90\]](#page-17-1). The total power is sensed by a photodetector (O-E), making this fan-in approach compatible with the O-E-O approach to cascadability. Multiwavelengthweighted addition was combined with O-E-O laser neurons in Ref. [\[48,](#page-15-9)[50\]](#page-15-13), wherein cascadability was also considered but not directly demonstrated. A downside of relying on multiple wavelengths is the need for a dieter laser source for each channel. The size of a single all-to-all subnetwork is capped by the available spectrum and the ability to distinguish adjacent channels, found in Ref. [\[91\]](#page-17-2) to be less than 950 if using the resonators of Ref. [\[92\]](#page-17-3); however, multiples of these subnetworks could be interfaced on a single chip [\[55\]](#page-15-14).

B. Nonspiking photonic neurons

The great majority of the work on photonic neurons has focused on lasers that implement spiking models similar to biological neurons [\[42–](#page-15-15)[50\]](#page-15-13), reviewed in Ref. [\[51\]](#page-15-16). To claim a nonspiking modulator as a photonic neuron r

There is no fundamental reason why photonics must

is met when the neuron's di erential optical-to-optical gain, *g*, exceeds unity.

1. Theor

The gain *g* can be derived from the device properties. It is defined as follows:

$$
g = \frac{dP_{\text{out}}}{dP_{\text{in}}} \tag{A1}
$$

$$
= \frac{dP_{\text{out}}}{dT_{\text{mod}}} \frac{dT_{\text{mod}}}{dV} \frac{dV}{dP_{\text{in}}},
$$
(A2)

where T_{mod} is modulator transmission, and *V* is the junction

which crosses zero when

$$
P_{\text{pump}}|_{J=0} = \frac{2V}{R_{\text{pd}}R_b}.\tag{A16}
$$

Thus, the expression for the pump power where the autapse loses monostability corresponds exactly with that where learning under hardware constraints—using a RISC pro[cessor embedded in a neuromorphic substrate,](https://doi.org/10.3389/fnins.2013.00160) Front. Neurosci. **7**, 00 (2013).

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